Company Name	Compound Semiconductor Research Center	Company Logo
Address	1F, External Cooperation Building, Mokpo National University, 1666 Yeongsan-ro, Cheonggye-myeon, Muan-gun, Jeollanam-do, Republic of Korea	
President	Hee Sauk Jhon	
Website	https://csrc.or.kr/	
E-mail	Kindro1@mnu.ac.kr	
Telephone	010-9703-7000	
Fax	-	
Exhibitor Introduction	The Compound Semiconductor Research Center (CSRC) is a specialized research and support institution established by Jeollanam-do to foster the region's strategic industries. The center aims to strengthen the local industrial foundation and enhance technological competitiveness in next-generation communication and power sectors by operating comprehensive programs that include GaN/GaAs compound semiconductor device development, measurement and verification infrastructure, corporate technical support, and professional workforce training. In addition, through collaboration with local universities, companies, and research institutes, CSRC plays a key role in building and revitalizing the semiconductor ecosystem in the Jeollanam-do region.	
Exhibit Description	CSRC's MPW (Multi-Project Wafer) program provides cost-effective and accessible GaN and GaAs fabrication opportunities for universities, research institutes, and companies developing RF and power semiconductor devices. Through partnerships with leading foundries, the program offers shared wafer runs, PDK support, design consultation, and measurement verification services. The MPW initiative aims to accelerate prototype development, reduce fabrication barriers, and expand the compound semiconductor ecosystem in the Jeollanam-do region and beyond.	

Exhibit Product	1. MPW Program Information - Introduction to Win Semiconductors GaN/GaAs MPW services - Introduction to Wavice GaN MPW services - Details on process specifications, application procedures, schedules, pricing, and available technical support 2. PDK and Design Flow Materials - Overview of WIN and Wavice PDK components and installation - Design environment setup (ADS/Cadence) and tool flow guidance - Explanation of DRC, LVS, EM-based verification and optimization processes 3. Technical Support & Collaboration Programs - Information on CSRC's design support, measurement support, and technical assistance framework - Introduction to academia–industry collaboration programs based on MPW participation - Overview of training, hands-on practice, workforce development, and recruitment-linked programs