

Session Title: [WA1] RF Circuit Techniques for Wideband and Multi-Band

**Systems** 

Session Date: December 3 (Wed.), 2025

Session Time: 13:20-15:00

Session Room: Room A (Halla A)

[WA1-1] [Invited] 13:20-13:40

## Broadband LNA Design Utilizing an Interleaved-Dual-Zero/Pole-Pair Technique

Yuyang Chen, Hanqi Gao, Xiaoming Liu, Chao Yang, Jianjun Zhou and Jing Jin (Shanghai Jiao Tong University, China)

[WA1-2] 13:40-14:00

## A 57-110 GHz LNA with Novel Bandwidth Enhancement Technique in 130-nm SiGe BiCMOS

Zhan Chen (Nanjing University of Science and Technology, China); Chunxia Zhou (Nanjin University of Science and Technology, China); Guoxiao Cheng, Wei Kang and Wen Wu (Nanjing University of Science and Technology, China)

[WA1-3] 14:00-14:20

## An 18/28 GHz Reconfigurable GaN LNA with Dual-Input Stage for Multi-Band Communications

Dingyuan Zeng, Haoshen Zhu, Xin He, Zeqi Liu, Song Chen and Xuelong Chen (South China University of Technology, China); Zongqi Cai (CEPREI, China); Quan Xue (South China University of Technology, China)

[WA1-4] 14:20-14:40

## A 26-GHz CMOS Push-Push Oscillator

Hiyori Kishimoto, Kiyotaka Komoku, Jun Furuta, Yasunori Suzuki and Nobuyuki Itoh (Okayama Prefectural University, Japan)

[WA1-5] 14:40-15:00

A 3.5-GHz Duo-Binary-Encoding-Assisted Envelope Delta-Sigma Digitized Transmitter for Bandwidth-Efficient Three-Level-Envelope Power Amplification

Seunghyun Jang (ETRI, Korea (South))