

Session Title: [TA2] RF Front-End Circuits for Reconfigurable and mmWave

Applications

Session Date: December 4 (Thu.), 2025

Session Time: 10:30-12:10

Session Room: Room A (Halla A)

Session Chair(s): Shinji Hara (Nagoya University, Japan)

Jinseok Park (Chonnam National University, Korea)

[TA2-1] [Invited] 10:30-10:50

A Design of Sub-GHz Wi-SUN/ TVWS/Dual Mode Transceiver with Automatic Impedance Calibration

Sung Jae Lee (SungKyunKwan University & SKAICHIPS Company, Korea (South)); Young Gun Pu, Jun-Eun Park and Kang-Yoon Lee (Sungkyunkwan University, Korea (South))

[TA2-2] 10:50-11:10

Low-Loss, Compact Band-Pass/Low-Pass Switch-Type 4-Bit Phase Shifter for 5G FR2 Applications

Geonho Park, Kyunghwan Kim, Goeun Baek and Doyoon Kim (Samsung Electronics, Korea (South)); Han-Woong Choi (Chunnam National Univiersity, Korea (South)); Byeong-Taek Moon (KAIST, Korea (South)); Hyun-Chul Park and Chan-Hong Park (Samsung Electronics, Korea (South))

[TA2-3] 11:10-11:30

24-GHz CMOS Active Quasi-Circulator with Wideband 30-dB Isolation

Chih-Hao Yang, Zi-Hao Fu and Kun-You Lin (National Taiwan University, Taiwan)

[TA2-4] 11:30-11:50

A E Band Vector Sum Phase Shifter with Switched Delay Line

Jun-kyo Park, Ye-Won Jeon, Sun-Kyu Kim and Seuk-Won Kang (Sungkyunkwan University, Korea (South)); Sungho Lee (Korea Electronics Technology Institute, Korea (South)); Byung-Sung Kim (Sungkyunkwan University, Korea (South))

[TA2-5] 11:50-12:10

A Compact Active Bi-Directional Phase Shifter Employing a Highly Isolated Single Gilbert-Cell

Uichan Park, Taeyeong Yoon and Jungsuek Oh (Seoul National University, Korea (South))